1.3 GHz Output +3 [dBm] N

1.3 GHz Output +3 [dBm] N

50 [MHz] LVDS + RJ45 1

50 [MHz] LVDS − RJ45 2

12,5 [MHz] LVDS + RJ45 7

12,5 [MHz] LVDS − RJ45 8

1,3 GHz Ref. Input
10 [dBm]

N

SP1
Mini-Circuits
Splitter
ZBD4PD1-2000+
−6,5 [dB]

1 S 2 3 4

J5 OUT1
J10 3,3 [V]
J6 OUT1B
J7 CLK
J3 OUT0
J4 OUT0B
J10

DV1
Analog Devices
Evaluation Board
AD9515
/ 26

J1 OUT1
J10 3,3 [V]
J6 OUT1B
J4 OUT0B
J7 CLK
J3 OUT0
J10

DV2
Analog Devices
Evaluation Board
AD9515
/ 4

12,5 [MHz] PECL − SMA

50 [MHz] PECL − SMA

+5 [V]

LP1
Mini-Circuits
Lowpass
SLP-15+

AMP2
Mini-Circuits
Amplifier
ZX60-2411 BM-S
+12 [dB]

OUT 1.2875 GHz Output +12 [dBm] N

IN

IF

MIX1
Mini-Circuits
Mixer
ZX05-5
−7 [dB]

LO RF

BP1
ID-Elektronik
1,2875 GHz Filter
−1 [dB]

IN

3,3 [V]

IN

AMP1
Mini-Circuits
Amplifier
ZX60-33LN+
+17 [dB]

OUT

1

SP1
Mini-Circuits
Splitter
ZBD4PD1-2000+
−6,5 [dB]

S

2 1.2875 GHz Output +0 [dBm] N

3 1.2875 GHz Output +0 [dBm] N

4 1.2875 GHz Output +0 [dBm] N

+5 [V] T2A +5 [V]

GND

SP1
3 x
LT1763-3.3

3,3 [V]

3,3 [V]

3,3 [V]

				Masstab	
			Datum	Name	Block Diagram Of The Master Oscillator Signal Box
			Bearb. 180610	K. .Ludwig	
			Gepr.		
			Norm DIN 6771		
			Firma des Zeichnungs-erstellers HZB		REV 03
					Blatt 1
					1 Bl.
Zust	Aenderung	Datum	Name		